300	FREQUENCY SHIFT KEYING OR MINIMUM SHIFT KEYING DEMODULATOR	324	Input signal applied directly to local oscillator
301	.Including discrete semiconductor device	325	Including phase or frequency locked loop
302	.Input signal combined with local oscillator or carrier	326	Including discrete semiconductor device
303	<pre>frequency signal .Including logic element (e.g.,</pre>	327	.Input signal split into plural signals
304	logic gate or flip-flop PHASE SHIFT KEYING OR QUADRATURE	328	Including piezoelectric resonant element
305	AMPLITUDE DEMODULATOR .Including discrete semiconductor	329	Using a transformer to split input signal
306	device	330	Signals taken from primary and secondary windings
300	.Input signal combined with local oscillator or carrier frequency signal	331	Including plural detector diodes (e.g., ratio detector
307	Including phase or frequency locked loop	332	type)Signals taken from plural
308	With parallel signal combiners (e.g., Costas loop)	333	secondary windingsIncluding plural detector diodes (e.g., ratio detector
309	Including logic element (e.g., logic gate or flip-	334	type)Including resonant circuits
310	<pre>flop) .Including logic element (e.g., logic gate or flip-flop)</pre>	331	tuned above and below input signal carrier frequency
311	PULSE OR INTERRUPTED CONTINUOUS WAVE DEMODULATOR	335	Including plural detector diodes (e.g., ratio detector
312	.Pulse width demodulator		type)
313	.Pulse position, frequency, phase or spacing demodulator	336	Input signal combined with phase shifted or delayed
314	Including discrete semiconductor device	337	portion of input signalIncluding LC resonant phase
315	FREQUENCY MODULATION DEMODULATOR		shift circuit
316	.Plural demodulation	338	Input signal split by LC
317	Including amplitude demodulation	339	resonant circuitIncluding LC resonant circuits
318	.Having specific distortion, noise or other interference		tuned above and below input signal carrier frequency
	prevention, reduction or compensation	340	<pre>Including plural detector diodes (e.g., ratio detector type)</pre>
319	Using feedback to reduce distortion, noise or other interference	341	.Input signal converted to and processed in pulse form (e.g.,
320	By cancelling distortion, noise or other interference		<pre>pulse counter or digital type demodulator)</pre>
321	Using amplitude limiting within the demodulator	342	Including discrete semiconductor device
322	.Including microwave or distributed parameter	343	Including digital logic circuitry
323	structure .Input signal combined with local	344	.Including signal passed through single demodulating path
	oscillator or carrier frequency signal	345	including resonant circuit PHASE MODULATION DEMODULATOR

346	.Input signal combined with local oscillator or carrier	370	.Including diode demodulator device
	frequency signal	371	.Coherer type demodulator
347	AMPLITUDE MODULATION DEMODULATOR	372	MISCELLANEOUS
348	.Having plural demodulation outputs		
349	.Having specific distortion,		
	noise or other interference	FOREIC	N ART COLLECTIONS
	prevention, reduction or		
	compensation	FOR	CLASS-RELATED FOREIGN DOCUMENTS
350	Automatic amplitude		
251	stabilization or control		
351	Nonlinearity reduction or compensation		
352	Temperature compensation		
353	Noise reduction or compensation		
354	.Including microwave or		
	distributed parameter structure		
355	With electron discharge device		
356	.Suppressed carrier double		
	sideband type		
357	.Single or vestigial sideband		
	type		
358	.Input signal combined with local		
	oscillator or carrier		
	frequency signal (e.g.,		
	synchronous demodulator)		
359	Input signal applied directly		
	to local oscillator		
360	Including phase or frequency		
	locked loop		
361	Including sampling, gating, or switching		
362	With three or more terminal		
	discrete semiconductor device		
363	.Input signal split into plural		
	signals		
364	Including three or more		
	terminal discrete		
	semiconductor device		
365	Including diode demodulator		
266	device		
366	Plural diodes		
367	.Including regenerative feedback		
260	in non-oscillating demodulator		
368	.Electron discharge demodulator		
	device having more than two		
	<pre>electrodes (e.g., triode, tetrode etc.)</pre>		
369	,		
J U J	.Including three or more terminal discrete semiconductor		
	demodulator device		
	acmodatator acvice		